

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	3020	branch near3 target	US-PGPUB; USPAT	OR	OFF	2004/11/12 09:16
S3	480	S2 and ((stall near2 (bit or field)) or (prefetch near2 (bit or field)) or (memory near2 line))	US-PGPUB; USPAT	OR	OFF	2004/11/12 09:19
S4	31	S2 and ((stall near2 (bit or field)))	US-PGPUB; USPAT	OR	OFF	2004/11/12 09:19
S6	242	(712/237).CCLS.	US-PGPUB; USPAT	OR	OFF	2004/11/16 17:28
S7	94	S6 and ((memory near2 line\$1) or (boundary or boundaries))	US-PGPUB; USPAT	OR	OFF	2004/11/12 09:30
S8	55	S2 same (cross\$3 near3 (boundar\$3 or line\$1))	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:18
S9	714	branch same (cross\$3 near3 (boundar\$3 or line\$1))	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:19
S10	393	branch with (cross\$3 near3 (boundar\$3 or line\$1))	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:19
S11	111	(branch with (cross\$3 near3 (boundar\$3 or line\$1))) and (processor or instruction)	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:19
S12	69	S11 not (S7 or S8)	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:21
S13	102	S9 and (fetch\$3 or prefetch\$3)	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:25
S14	1635	(fetch\$3 or prefetch\$3) same (instruction\$1 or branch or target\$1) same (memory near2 line or boundary or cross\$3)	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:27
S15	160	(fetch\$3 or prefetch\$3) same (instruction\$1 or branch or target\$1) same (((memory near2 line) or boundary) with cross\$3)	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:27
S16	18	S15 and vliw	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:32
S17	228	(712/24).CCLS.	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:33
S18	15	S17 and ((fetch\$3 or prefetch\$3) with (branch near3 target\$1))	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:39
S19	60	(stall near2 (bit or bits or field\$1 or flag\$1)) and (branch near2 target) and (fetch\$3)	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:41
S20	9	(stall near2 (bit or bits or field\$1 or flag\$1)) same (branch near2 target) same (fetch\$3 or prefetch\$3)	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:43

S21	9	(stall\$3 near2 (bit or bits or field\$1 or flag\$1)) same (branch near2 target) same (fetch\$3 or prefetch\$3)	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:44
S22	192	(stall\$3) same (branch near2 target) same (fetch\$3 or prefetch\$3)	US-PGPUB; USPAT	OR	OFF	2004/11/12 11:44
S23	128	(stall\$3) same (branch near2 target) with (fetch\$3 or prefetch\$3)	US-PGPUB; USPAT	OR	OFF	2004/11/12 13:13
S24	3	((branch near2 target) with (prefetch\$3)) same (unaligned or odd or misaligned)	US-PGPUB; USPAT	OR	OFF	2004/11/12 13:17
S25	2	("5729727").URPN.	USPAT	OR	OFF	2004/11/12 13:27
S26	6	("4488227" "4933841" "5034880" "5088030" "5148532" "5526498").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/12 13:34
S27	72	((header or predecode or align\$4 or prefix) near3 (field or bit or bits of flag\$1)) same (pad or padded or padding) same (counter\$1 or pointer\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/12 13:41
S28	22	((header or predecode or align\$4 or prefix) near3 (field or bit or bits of flag\$1)) with instruction) and (pad or padded or padding) same (counter\$1 or pointer\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/12 13:54
S29	95	((bit or field or bits or flags) same ((boundary or line or memory) with cross\$3)) and vliw	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/12 14:15
S30	25	(fetch\$3 or prefetch\$3) and ((header or predecode or prefix or (bit near2 field)) with \$3align\$4) and vliw	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/12 14:21
S31	6	(fetch\$3 or prefetch\$3) same ((header or predecode or prefix or (bit near2 field)) with \$3align\$4) and vliw	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/12 14:25
S32	162	(branch near2 target) with (cross\$3 or odd or misalign\$4 or unalign\$4 or align\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/12 14:26
S33	87	(branch near2 target) with (cross\$3 or odd or misalign\$4 or unalign\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/12 14:29
S34	61	(branch near2 target) with (cross\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/12 14:30
S35	71	(branch near2 target) with (cross\$3 or overlap\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/12 14:32

S52	130	S51 and ((branch near2 target) or (cross\$3 near3 (boundary or line)))	US-PGPUB; USPAT	OR	OFF	2004/11/12 16:57
S53	17	S51 and ((branch near2 target) and (cross\$3 near3 (boundary or line)))	US-PGPUB; USPAT	OR	OFF	2004/11/12 17:14
S54	1	((update or move or skip or forward) near2 ((program near1 counter) or (instruction near1 pointer))) with (pad or padded or padding)	US-PGPUB; USPAT	OR	OFF	2004/11/12 17:15
S55	1	((update or move or skip or forward) near2 ((program near1 counter) or (instruction near1 pointer))) same (pad or padded or padding)	US-PGPUB; USPAT	OR	OFF	2004/11/12 17:15
S56	5	S41 and ((branch or target) with (cross\$3 near3 (boundar\$3 or line\$1)))	US-PGPUB; USPAT	OR	OFF	2004/11/12 17:20
S57	13	("4437149" "4547849" "4823286" "5057837" "5109523" "5179680" "5265258" "5303357" "5381531" "5471593" "5530817" "5652852" "5669001").PN. OR ("6704859").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/12 17:26
S58	13	S57 and (branch or target or cross\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/15 09:44
S60	182	prefetch and stall and branch and target and pad\$3	US-PGPUB; USPAT	OR	OFF	2004/11/15 16:18
S63	5	((("5983336") or ("5634025") or ("5881260") or ("6704859") or ("6044450")).PN.	US-PGPUB; USPAT	OR	OFF	2004/11/15 17:26
S65	2596930	prefetch and stall adj3 bit or field or bits	US-PGPUB; USPAT	OR	OFF	2004/11/15 17:27
S66	28	prefetch and (stall adj3 (bit or field or bits))	US-PGPUB; USPAT	OR	OFF	2004/11/15 17:28
S67	4	prefetch same (stall adj3 (bit or field or bits))	US-PGPUB; USPAT	OR	OFF	2004/11/15 17:30
S69	10	(fetch\$3 same (stall\$3 adj3 (bit or field or bits))) not S67	US-PGPUB; USPAT	OR	OFF	2004/11/15 17:35
S70	384	((stall\$3 adj3 (bit or field or bits))) not S67	US-PGPUB; USPAT	OR	OFF	2004/11/15 17:36
S71	137	((stall\$3 adj3 (bit or field or bits)) same instruction) not S67	US-PGPUB; USPAT	OR	OFF	2004/11/15 17:36
S72	133	((stall\$3 near3 (bit or field or bits)) with instruction) not S67	US-PGPUB; USPAT	OR	OFF	2004/11/15 17:37
S73	10651	("712").CLAS.	US-PGPUB; USPAT	OR	OFF	2004/11/15 17:37

S10 2	512	(pad\$4 with (counter or pointer or pc) with (increment\$3 or updat\$3 or increas\$3 or align\$4 or realign\$4))	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/16 17:18
S10 3	149	S102 and (processor or instruction)	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/16 17:17
S10 6	299	((updat\$3 or increas\$3 or increment\$3 or align\$4 or realign\$4) near3 (pointer or counter or pc)) same ((padding or padded or pad) and (bits or bit or field or instruction or line))	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/16 17:26
S10 7	262	((updat\$3 or increas\$3 or increment\$3 or align\$4 or realign\$4) near3 (pointer or counter or pc)) same ((padding or padded or pad) same (bits or bit or field or instruction or line))	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/16 17:27
S10 8	96320	((padding or padded or pad) same (bits or bit or field or instruction or line))	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/16 17:27
S10 9	80750	((updat\$3 or increas\$3 or increment\$3 or align\$4 or realign\$4) near3 (pointer or counter or pc))	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/16 17:27
S11 0	262	S108 same S109	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/16 17:27
S11 1	10662	("712").CLAS.	US-PGPUB; USPAT	OR	OFF	2004/11/16 17:28
S11 2	30	S110 and S111	US-PGPUB; USPAT	OR	OFF	2004/11/16 17:28
S11 3	13	S112 not "scratch pad"	US-PGPUB; USPAT	OR	OFF	2004/11/16 17:28
S11 6	6	"418098"	US-PGPUB; USPAT	OR	OFF	2004/11/17 09:26
S11 7	5467	keller.in.	US-PGPUB; USPAT	OR	OFF	2004/11/17 09:26
S11 8	431	S117 and processor	US-PGPUB; USPAT	OR	OFF	2004/11/17 11:29
S12 3	40	vliw and \$2compress\$3 and ((padd\$3 or pad) adj2 bit\$1)	US-PGPUB; USPAT	OR	OFF	2004/11/18 10:05
S12 5	14	("5819058").URPN.	USPAT	OR	OFF	2004/11/18 15:53
S12 6	28	align\$2 near2 instruction\$1 with (fast\$2 or efficient or better) with (fetch\$3 or decod\$3 or process\$3)	USPAT	OR	OFF	2004/11/18 16:09